

FIG. 1

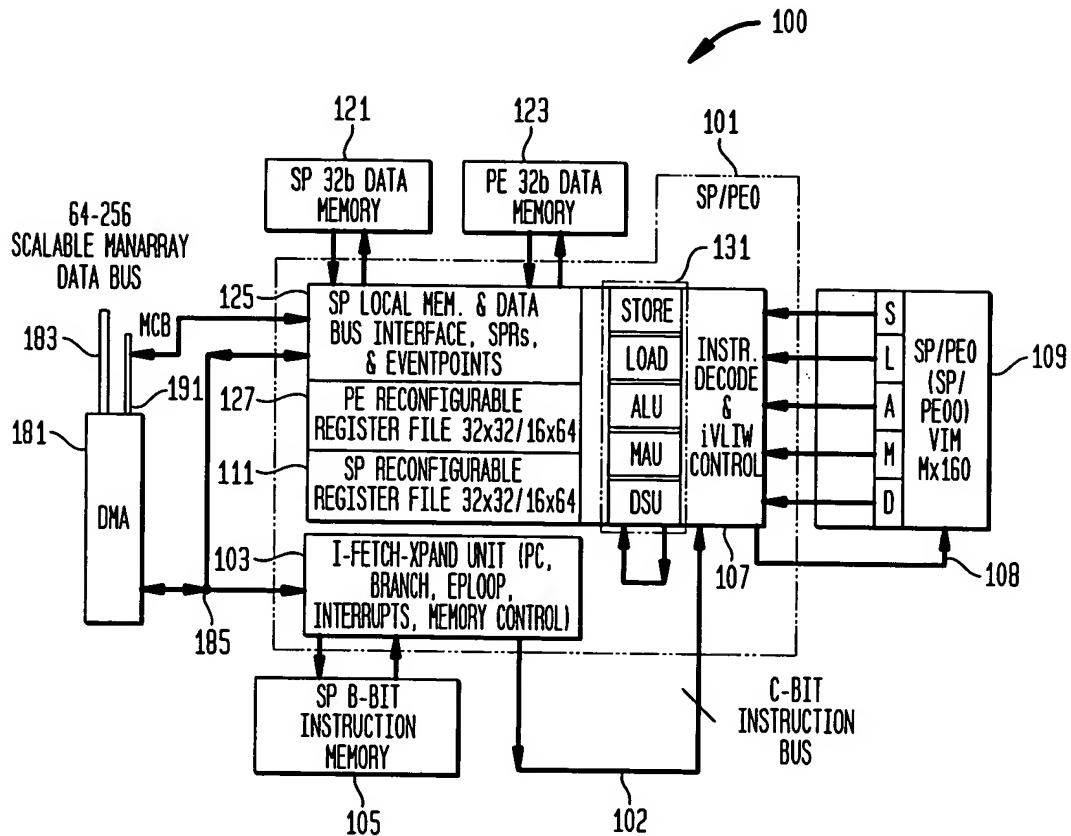


FIG. 2

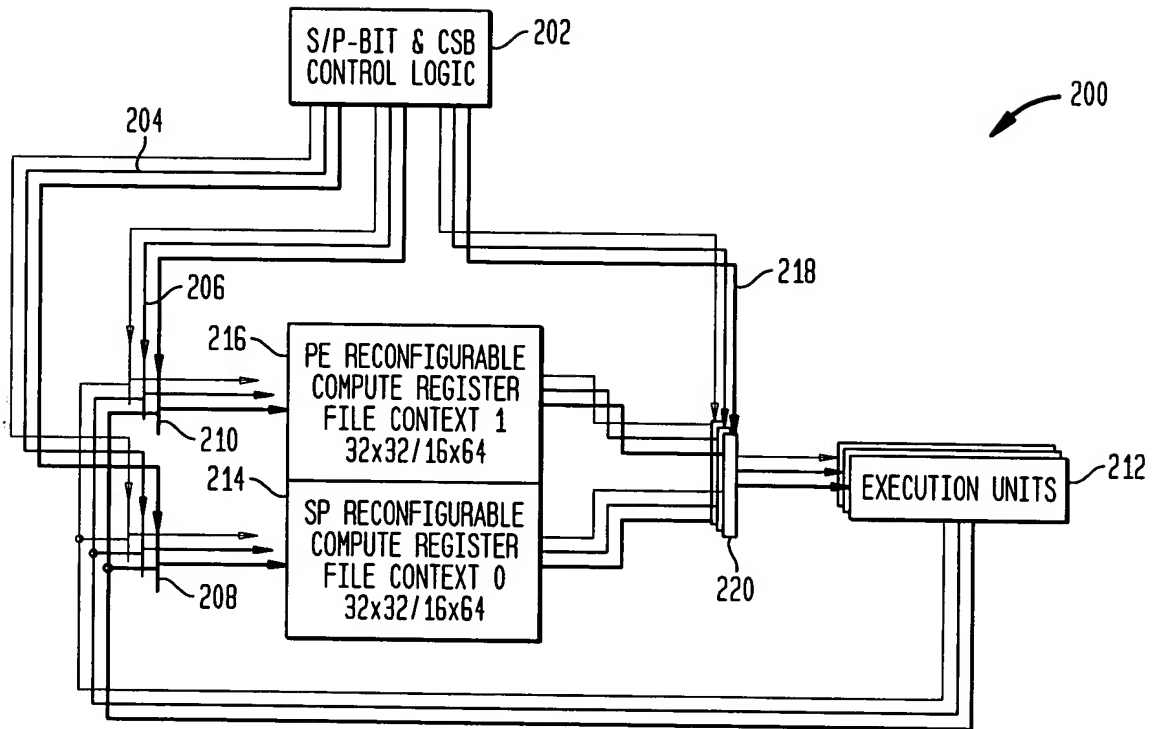


FIG. 3

MANARRAY ARCHITECTURE INSTRUCTION TYPE			
S/P	CSB	S/P	OPERATING MxN
0	SP ONLY INSTRUCTION	0	1x1
1	PE ONLY INSTRUCTION	1	1x1

304	PHYSICAL MxN	CSB	S/P	OPERATING MxN	COMMENTS
320	1x1	0	0	1x1	SP INSTRUCTIONS USE ONLY THE SP's CONTEXT-0 REGISTER FILES
322	1x1	0	1	1x1	PE INSTRUCTIONS USE ONLY THE PE0's REGISTER FILES
324	1x1	1	0	1x0	SP INSTRUCTIONS USE ONLY THE PE0's CONTEXT-1 REGISTER FILES
326	1x1	1	1	1x0	PE INSTRUCTIONS TYPICALLY NOT USED
330	1x2	0	0	1x2	SP INSTRUCTIONS USE ONLY THE SP's CONTEXT-0 REGISTER FILES
332	1x2	0	1	1x2	PE INSTRUCTIONS USE ONLY THE PE0's AND PE1's REGISTER FILES
334	1x2	1	0	1x1	SP INSTRUCTIONS USE ONLY THE PE0's CONTEXT-1 REGISTER FILES
336	1x2	1	1	1x1	PE INSTRUCTIONS USE ONLY THE PE1's REGISTER FILES
340	1x5	0	0	1x5	SP INSTRUCTIONS USE ONLY THE SP's CONTEXT-0 REGISTER FILES
342	1x5	0	1	1x5	PE INSTRUCTIONS USE ONLY THE PE0-4's REGISTER FILES
344	1x5	1	0	2x2	SP INSTRUCTIONS USE ONLY THE PE0's CONTEXT-1 REGISTER FILES
346	1x5	1	1	2x2	PE INSTRUCTIONS USE ONLY THE PE1-4's REGISTER FILES

300

FIG. 4

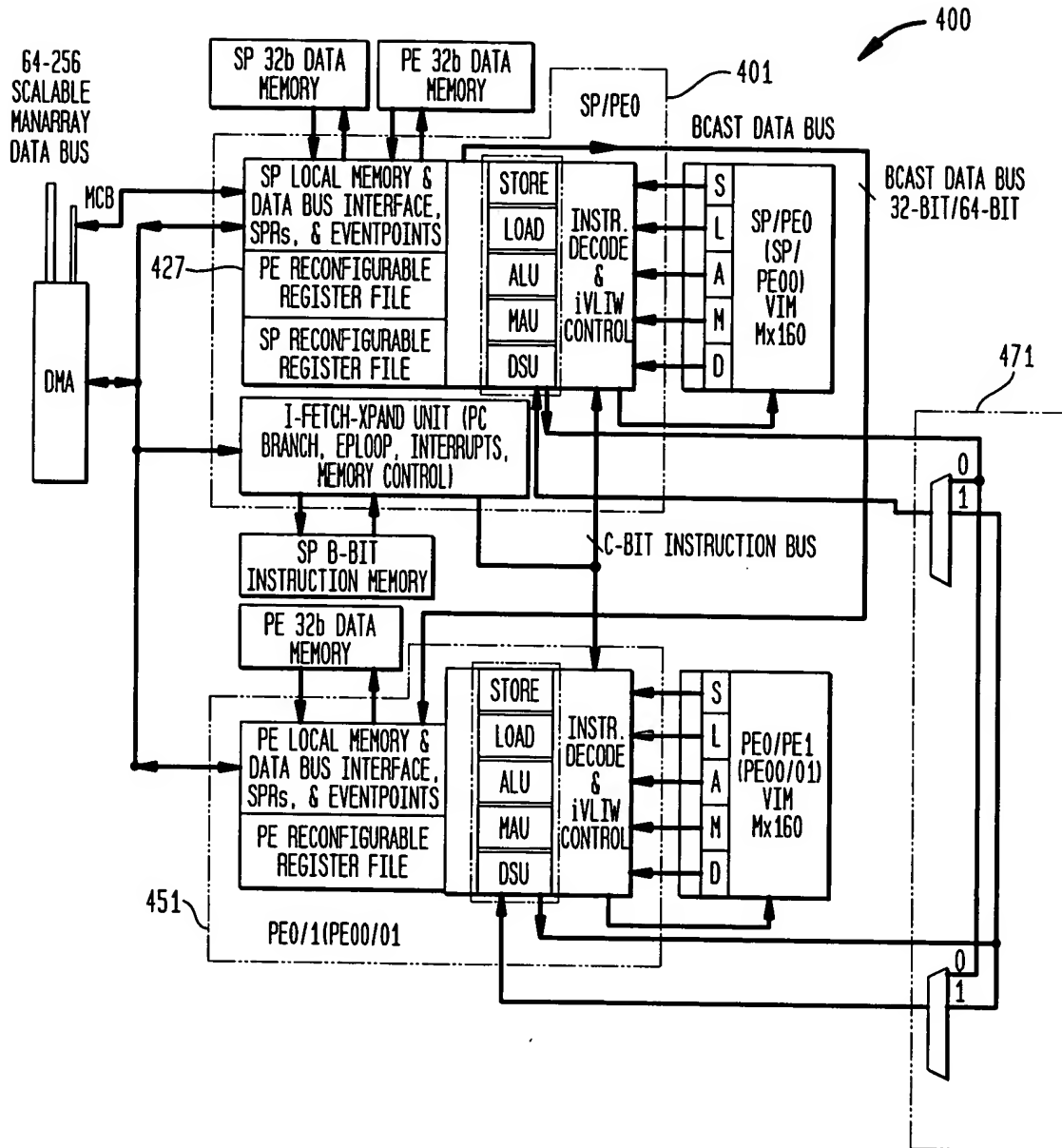


FIG. 5A

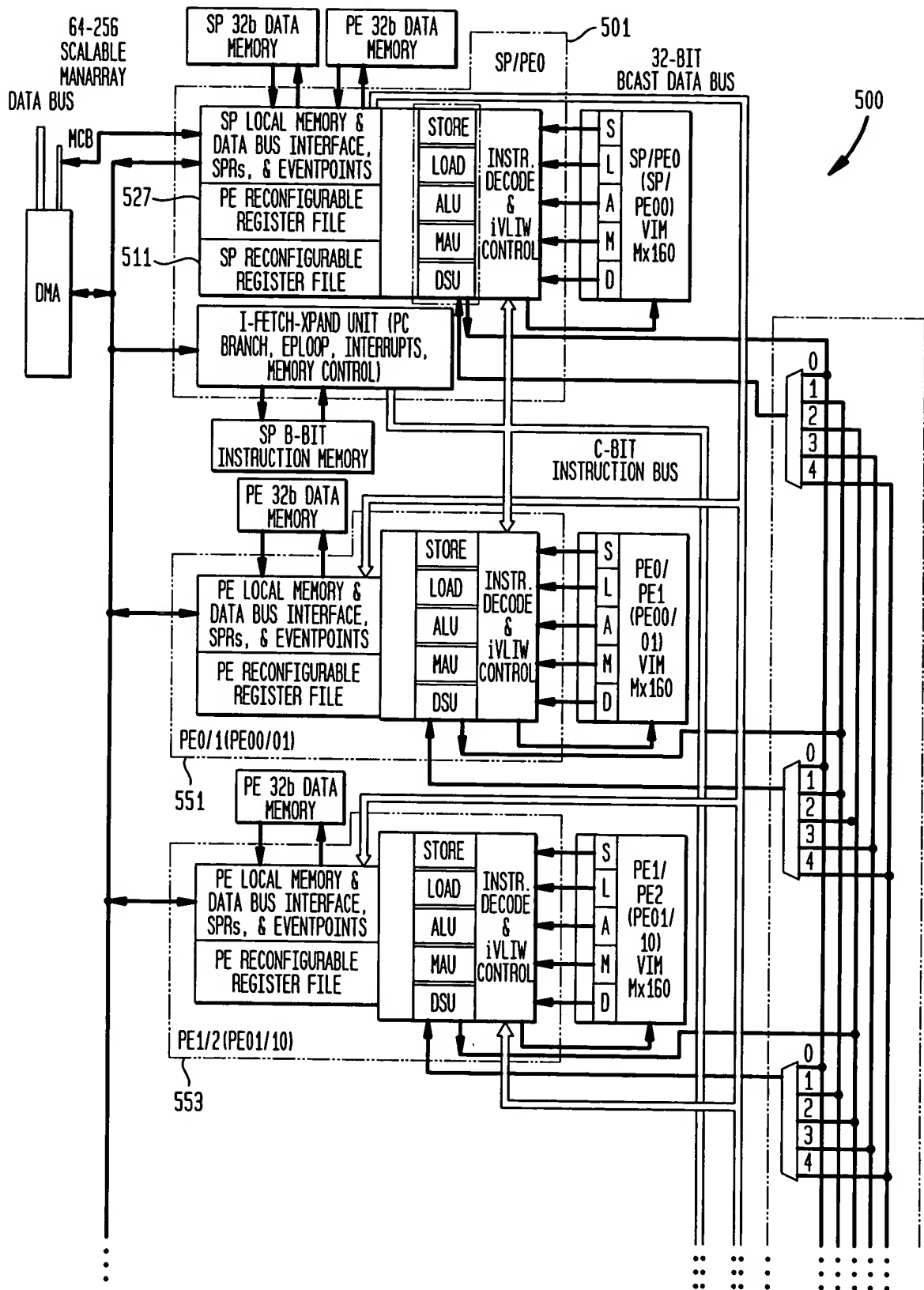


FIG. 5B

